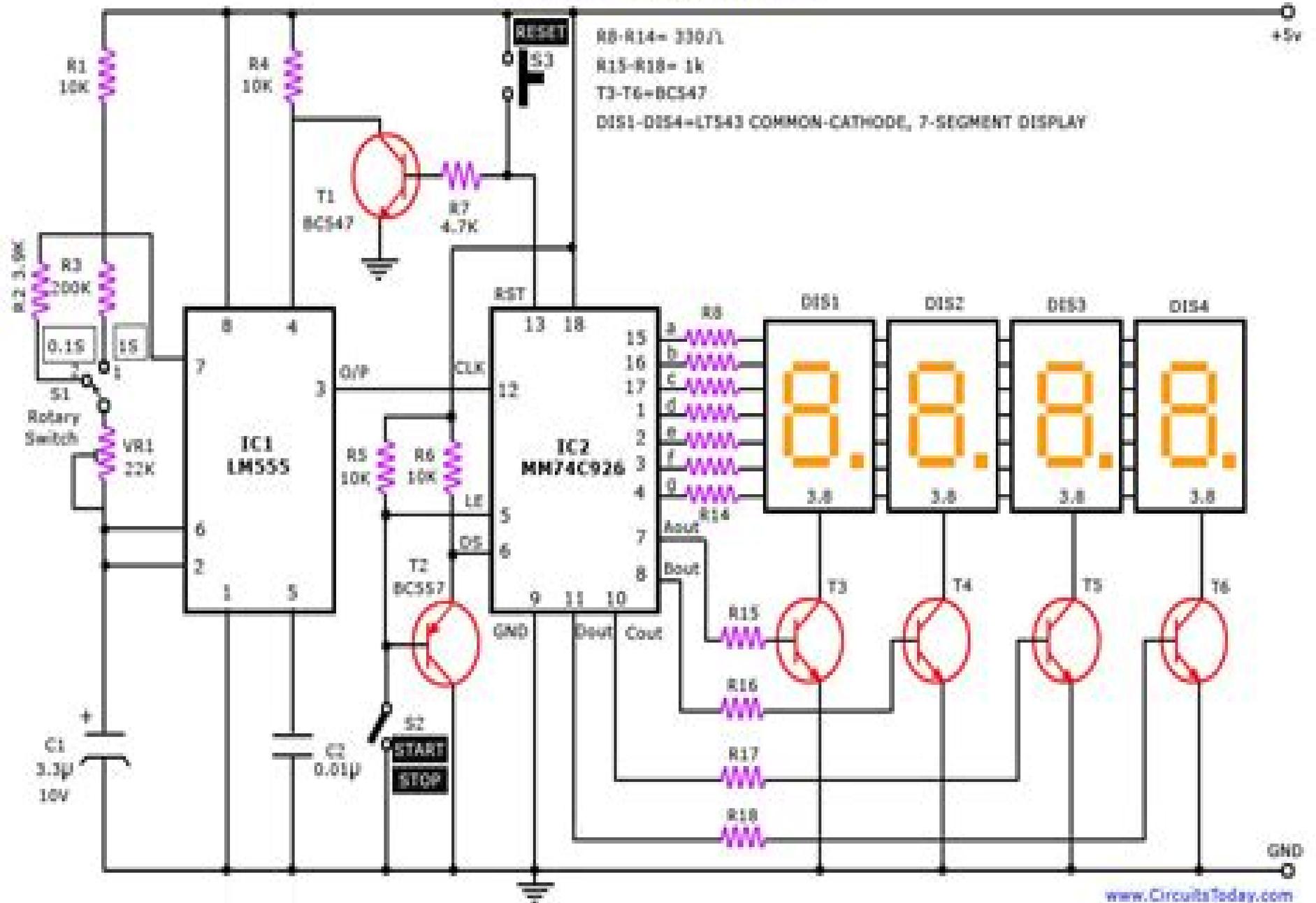


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Digital Timing Circuits

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Technical Abstract Bulletin , Applications of Walsh Functions , 1970 *Official Gazette of the United States Patent and Trademark Office* , 1975 The Logic Design of Transistor Digital Computers Gerald A. Maley, John Earle, 1963 1984 IEEE International Symposium on Circuits and Systems Proceedings , 1984 Electronic Design with Integrated Circuits David J. Comer, 1981 **An Introductory Guide to EC Competition Law and Practice** Valentine Korah, 1994 A Timing Model of Synchronous Digital Circuits Kareem A. Sakallah, Trevor N. Mudge, Oyekunle Ayinde Olukotun, University of Michigan. Department of Electrical Engineering and Computer Science, 1990 Abstract The need for accurate characterization of the timing behavior of synchronous digital systems has become increasingly evident in

the past few years The continuing drive to design faster digital systems has made the use of CAD tools for timing verification and optimal clocking essential in most system design methodologies Timing models of synchronous digital circuits are based on the premise that the circuits are logically functionally correct and focus only on capturing their propagation latching and synchronization properties

IEEE 1985 Microwave and Millimeter-Wave Monolithic Circuits Symposium Marvin Cohn,1985 *Electronic Circuit Design* C. J. Savant,Martin S. Roden,Gordon Lee Carpenter,1987 **101 Experiments for the Young Scientist** Dave Prochnow,1988 Conference Record ,1976 **A Collection of Papers** ,1981 **Industrial Research** ,1968 **Fundamentals of Electricity** Joel Goldberg,1981

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