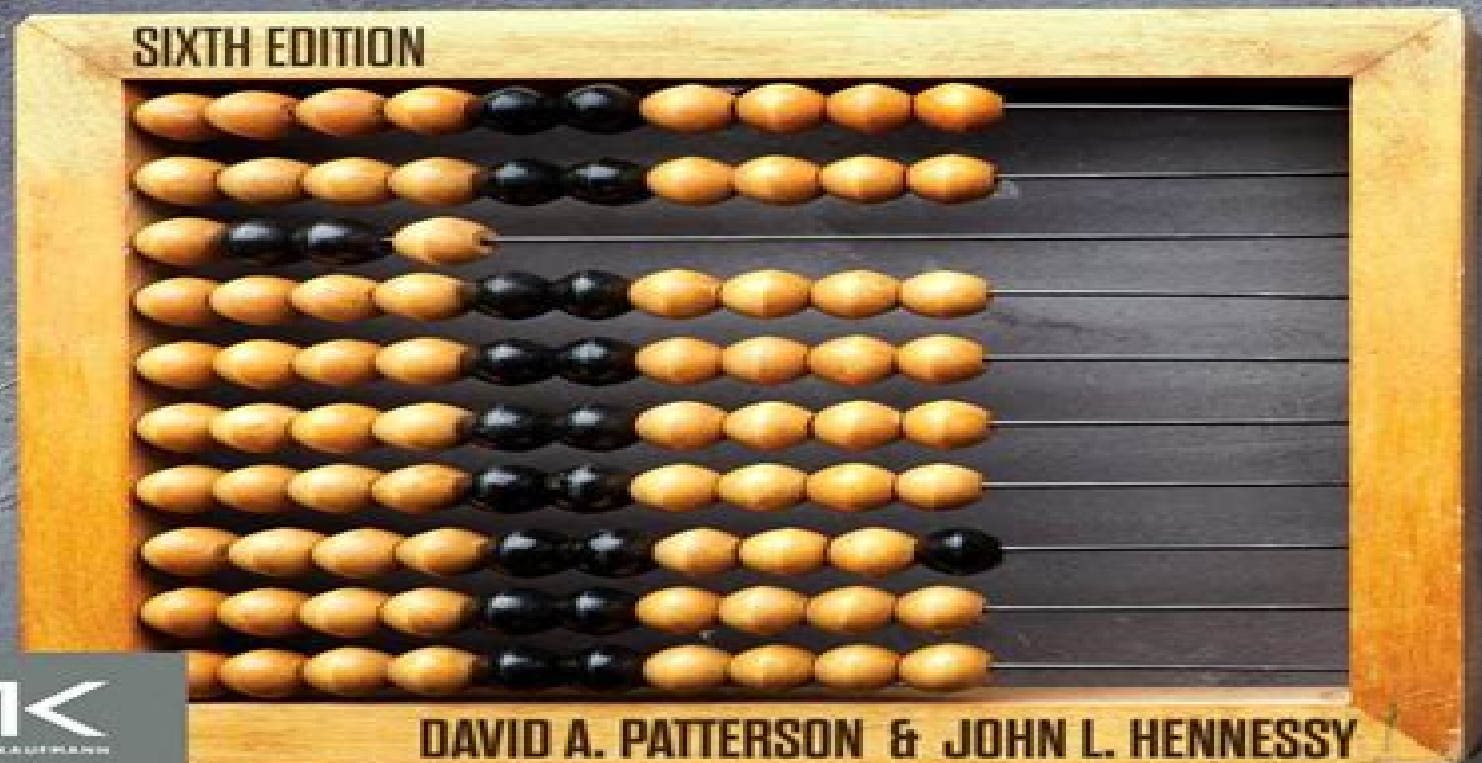


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Computer Organization and Design David A. Patterson, John L. Hennessy, 2004-08-07 This best selling text on computer organization has been thoroughly updated to reflect the newest technologies Examples highlight the latest processor designs benchmarking standards languages and tools As with previous editions a MIPS processor is the core used to present the fundamentals of hardware technologies at work in a computer system The book presents an entire MIPS instruction set instruction by instruction the fundamentals of assembly language computer arithmetic pipelining memory hierarchies and I O A new aspect of the third edition is the explicit connection between program performance and CPU performance The authors show how hardware and software components such as the specific algorithm programming language compiler ISA and processor implementation impact program performance Throughout the book a new feature focusing on program performance describes how to search for bottlenecks and improve performance in various parts of the system The book digs deeper into the hardware software interface presenting a complete view of the function of the programming language and compiler crucial for understanding computer organization A CD provides a toolkit of simulators and compilers along with tutorials for using them For instructor resources click on the grey companion site button found on the right side of this page This new edition represents a major revision New to this edition Entire Text has been updated to reflect new technology 70% new exercises Includes a CD loaded with software projects and exercises to support courses using a number of tools A new interior design presents defined terms in the margin for quick reference A new feature Understanding Program Performance focuses on performance from the programmer s perspective Two sets of exercises and solutions For More Practice and In More Depth are included on the CD Check Yourself questions help students check their understanding of major concepts Computers In the Real World feature illustrates the diversity of uses for information technology More detail below

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Computer Organization and Design, Revised Printing David A. Patterson, John L. Hennessy, 2007-06-06 What's New in the Third Edition Revised Printing The same great book gets better This revised printing features all of the original content along with these additional features Appendix A Assemblers Linkers and the SPIM Simulator has been moved from the CD ROM into the printed book Corrections and bug fixes Third Edition features New pedagogical features Understanding Program Performance Analyzes key performance issues from the programmer's perspective Check Yourself Questions Helps students assess their understanding of key points of a section Computers In the Real World Illustrates the diversity of applications of computing technology beyond traditional desktop and servers For More Practice Provides students with additional problems they can tackle In More Depth Presents new information and challenging exercises for the advanced student New reference features Highlighted glossary terms and definitions appear on the book page as bold faced entries in the index and as a separate and searchable reference on the CD A complete index of the material in the book and on the CD appears in the printed index and the CD includes a fully searchable version of the same index Historical Perspectives and Further Readings have been updated and expanded to include the history of software R D CD Library provides materials collected from the web which directly support the text In addition to thoroughly updating every aspect of the text to reflect the most current computing technology the third edition Uses standard 32 bit MIPS 32 as the primary teaching ISA Presents the assembler to HLL translations in both C and Java Highlights the latest developments in architecture in Real Stuff sections Intel IA 32 Power PC 604 Google's PC cluster Pentium P4 SPEC CPU2000 benchmark suite for processors SPEC Web99 benchmark for web servers EEMBC benchmark for embedded systems AMD Opteron memory hierarchy AMD vs IA 64 New support for distinct course goals Many of the adopters who have used our book throughout its two editions are refining their courses with a greater hardware or software focus We have provided new material to support these course goals New material to support a Hardware Focus Using logic design conventions Designing with hardware description languages Advanced pipelining Designing with FPGAs HDL simulators and tutorials Xilinx CAD tools New material to support a Software Focus How compilers work How to optimize compilers How to implement object oriented languages MIPS simulator and tutorial History sections on programming languages compilers operating systems and databases On the CD NEW Search

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