

Code Generation for Embedded Processors: An Introduction

P. Marwedel

1 New, flexible target technologies

As the tendency towards more complex electronic systems continues, many of these systems are equipped with embedded processors. For example, such processors can be found in cars, and in audio-, video-, and telecommunication-equipment. Essential advantages of these processors include their high flexibility, short design time and (in the case of off-the-shelf processors) full-custom layout quality. Furthermore, they allow an easy implementation of optional product features as well as easy design correction and upgrading. Furthermore, processors are frequently used in cases where the systems must be extremely *dependable*¹ [32]. In such cases, the re-use of the design of an off-the-shelf processor greatly simplifies dependability analysis.

This contrasts with the limitations of *application-specific circuits (ASICs)*: due to their low flexibility, the cost for the design and fabrication of ASICs is still very high. Furthermore, this low flexibility makes a short time-to-market more difficult to achieve. Dependability analysis costs may even exclude ASICs as a target technology.

A short time-to-market can be achieved with *field programmable gate arrays (FPGAs)*. But FPGAs are not area-efficient. For example, multipliers require a large proportion of the available area. Furthermore, FPGAs with programmable interconnect usually do not allow high clocking frequencies.

Embedded processors come in different types. We will classify them according to three different criteria: flexibility of the architecture, architectural features for certain application domains, and the form in which the processor is available. The three criteria can be used as dimensions to form a 3D processor type space (see fig. 1).

The meaning of these dimensions and their values is as follows:

1. *Architectural features for certain application domains*

Processors can be designed for restricted or for larger classes of application areas. The two cases considered here are: “*General purpose architecture (GPA)*” and “*digital signal processors (DSPs)*”.

The term “general purpose processor” is used for processors which do not have particular support for special applications, such as Fourier transforms or digital filtering.

¹This is a reprint of material from the book “P. Marwedel, G. Goossens (ed.): Code Generation for Embedded Processors, Kluwer, June 1995”. Kluwer copyright restrictions apply. Refer to the book for correct cross-references to other contributions in the book. This work has been supported by the Commission of the European Communities under contract BRA 9138 (CHIPS).

²The term *dependability* includes all aspects of system safety, for example: absence of design faults, comprehensive testing after manufacturing, reliable components, and error-detection and recovery mechanisms.

Code Generation For Embedded Processors

Xiang Xie



Code Generation For Embedded Processors:

Code Generation for Embedded Processors Peter Marwedel, Gert Goossens, 2013-03-14 Modern electronics is driven by the explosive growth of digital communications and multi media technology A basic challenge is to design first time right complex digital systems that meet stringent constraints on performance and power dissipation In order to combine this growing system complexity with an increasingly short time to market new system design technologies are emerging based on the paradigm of embedded programmable processors This concept introduces modularity flexibility and re use in the electronic system design process However its success will critically depend on the availability of efficient and reliable CAD tools to design programme and verify the functionality of embedded processors Recently new research efforts emerged on the edge between software compilation and hardware synthesis to develop high quality code generation tools for embedded processors Code Generation for Embedded Systems provides a survey of these new developments Although not limited to these targets the main emphasis is on code generation for modern DSP processors Important themes covered by the book include the scope of general purpose versus application specific processors machine code quality for embedded applications retargetability of the code generation process machine description formalisms and code generation methodologies Code Generation for Embedded Systems is the essential introduction to this fast developing field of research for students researchers and practitioners alike

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Proceedings of the ASP-DAC ... Asia and South Pacific Design Automation Conference, 2002

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High Performance Computing, 2000

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Proceedings of the 12th International Symposium on System Synthesis, 1999 These proceedings cover real time and low power system design performance issues in system design memory design for embedded systems architectural synthesis and system design methodologies

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