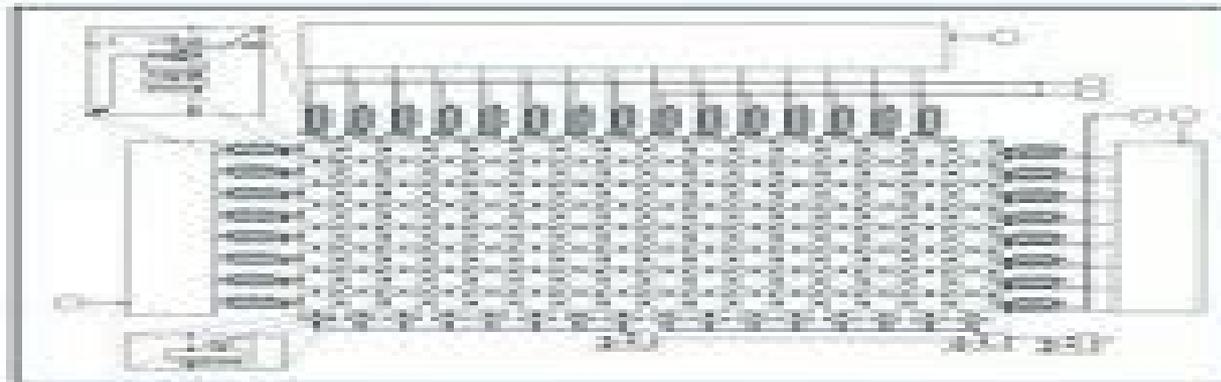

Application-Driven Architecture Synthesis

edited by

Francky Catthoor
Lars Svensson



Kluwer Academic Publishers

Application Driven Architecture Synthesis

K. J. Ray Liu, Kung Yao



Application Driven Architecture Synthesis:

Application-Driven Architecture Synthesis Francky Catthoor, Lars-Gunnar Svensson, 2012-12-06 Application Driven Architecture Synthesis describes the state of the art of architectural synthesis for complex real time processing In order to deal with the stringent timing requirements and the intricacies of complex real time signal and data processing target architecture styles and target application domains have been adopted to make the synthesis approach feasible These approaches are also heavily application driven which is illustrated by many realistic demonstrations used as examples in the book The focus is on domains where application specific solutions are attractive such as significant parts of audio telecom instrumentation speech robotics medical and automotive processing image and video processing TV multi media radar sonar Application Driven Architecture Synthesis is of interest to both academics and senior design engineers and CAD managers in industry It provides an excellent overview of what capabilities to expect from future practical design tools and includes an extensive bibliography

Modeling, Verification and Exploration of Task-Level Concurrency in Real-Time Embedded Systems Filip Thoen, Francky Catthoor, 2012-12-06 system is a complex object containing a significant percentage of elec A tronics that interacts with the Real World physical environments humans etc through sensing and actuating devices A system is heterogeneous i e is characterized by the co existence of a large number of components of disparate type and function for example programmable components such as micro processors and Digital Signal Processors DSPs analog components such as AID and D A converters sensors transmitters and receivers Any approach to system design today must include software concerns to be viable In fact it is now common knowledge that more than 70% of the development cost for complex systems such as automotive electronics and communication systems are due to software development In addition this percentage is increasing constantly It has been my take for years that the so called hardware software co design problem is formulated at a too low level to yield significant results in shorten ing design time to the point needed for next generation electronic devices and systems The level of abstraction has to be raised to the Architecture Function co design problem where Function refers to the operations that the system is supposed to carry out and Architecture is the set of supporting components for that functionality The supporting components as we said above are heteroge neous and contain almost always programmable components

Design of Hardware/Software Embedded Systems Eugenio Villar Bonet, 2001 Este libro presenta los desaf os planteados por las nuevas y sumamente poderosas tecnolog as de integraci n de sistemas electr nicos que est n en la base de los cambios sociales hacia lo que llaman la Sociedad de la Informaci n en la que los dispositivos electr nicos se har n una parte incorporada de la vida diaria encajados en casi cada producto Es necesario un conocimiento cuidadoso de los desaf os para aprovechar la amplia gama de ocasiones ofrecidas por tales capacidades de integraci n y las correspondientes posibilidades de dise o de sistemas electr nicos

Computer Aided Systems Theory - EUROCAST '97 Franz Pichler, Roberto Moreno-Diaz, 1997-11-05 This book constitutes a refereed post workshop selection of papers presented at the 6th

International Workshop on Computer Aided Systems Theory EUROCAST 97 held in Las Palmas de Gran Canaria Spain in February 1997 The 50 revised full papers presented were carefully selected for inclusion in the volume The book is divided into sections on design environments and tools theory and methods engineering systems intelligent systems signal processing and specific methods and applications Digital Signal Processing for Multimedia Systems Keshab K. Parhi, Takao Nishitani, 2018-10-03 Addresses a wide selection of multimedia applications programmable and custom architectures for the implementations of multimedia systems and arithmetic architectures and design methodologies The book covers recent applications of digital signal processing algorithms in multimedia presents high speed and low priority binary and finite field arithmetic architectures details VHDL based implementation approaches and more **Memory Issues in Embedded Systems-on-Chip** Preeti Ranjan Panda, Nikil D. Dutt, Alexandru Nicolau, 2012-12-06 Memory Issues in Embedded Systems On Chip Optimizations and Explorations is designed for different groups in the embedded systems on chip arena First it is designed for researchers and graduate students who wish to understand the research issues involved in memory system optimization and exploration for embedded systems on chip Second it is intended for designers of embedded systems who are migrating from a traditional micro controllers centered board based design methodology to newer design methodologies using IP blocks for processor core based embedded systems on chip Also since Memory Issues in Embedded Systems on Chip Optimization and Explorations illustrates a methodology for optimizing and exploring the memory configuration of embedded systems on chip it is intended for managers and system designers who may be interested in the emerging capabilities of embedded systems on chip design methodologies for memory intensive applications The Fifth International Workshop on Rapid System Prototyping Nick Kanopoulos, 1994 Proceedings of the June 1994 workshop presenting 25 papers on models for system simulation emulation in a hierarchical sense software to hardware mapping software prototyping and validation prototyping environments of hardware simulators and experiences from specific system prototyping projects Lacks an index Annotation copyright by Book News Inc Portland OR Light Propagation in Periodic Media Michel Neviere, Evgeny Popov, 2018-10-03 Based on more than 30 years of research on differential theories of gratings this book describes developments in differential theory for applications in spectroscopy acoustics X ray instrumentation optical communication information processing photolithography high power lasers high precision engineering and astronomy Introducing the Fast Fourier Factorization approach to improve the convergence of a truncated series the book examines multilayers stacked gratings crossed gratings photonic crystals and isotropic and anisotropic materials techniques and examples in grating design and Maxwell equations in a truncated Fourier space *IEEE Workshop on Signal Processing Systems*, 1997 **Network-on-Chip** Santanu Kundu, Santanu Chattopadhyay, 2018-09-03 Addresses the Challenges Associated with System on Chip Integration Network on Chip The Next Generation of System on Chip Integration examines the current issues restricting chip on chip communication efficiency and explores Network on chip NoC a promising

alternative that equips designers with the capability to produce a scalable reusable and high performance communication backbone by allowing for the integration of a large number of cores on a single system on chip SoC This book provides a basic overview of topics associated with NoC based design communication infrastructure design communication methodology evaluation framework and mapping of applications onto NoC It details the design and evaluation of different proposed NoC structures low power techniques signal integrity and reliability issues application mapping testing and future trends Utilizing examples of chips that have been implemented in industry and academia this text presents the full architectural design of components verified through implementation in industrial CAD tools It describes NoC research and developments incorporates theoretical proofs strengthening the analysis procedures and includes algorithms used in NoC design and synthesis In addition it considers other upcoming NoC issues such as low power NoC design signal integrity issues NoC testing reconfiguration synthesis and 3 D NoC design This text comprises 12 chapters and covers The evolution of NoC from SoC its research and developmental challenges NoC protocols elaborating flow control available network topologies routing mechanisms fault tolerance quality of service support and the design of network interfaces The router design strategies followed in NoCs The evaluation mechanism of NoC architectures The application mapping strategies followed in NoCs Low power design techniques specifically followed in NoCs The signal integrity and reliability issues of NoC The details of NoC testing strategies reported so far The problem of synthesizing application specific NoCs Reconfigurable NoC design issues Direction of future research and development in the field of NoC Network on Chip The Next Generation of System on Chip Integration covers the basic topics technology and future trends relevant to NoC based design and can be used by engineers students and researchers and other industry professionals interested in computer architecture embedded systems and parallel distributed systems

Proceedings of the Fourth Euromicro Workshop on Parallel and Distributed Processing (PDP '96) ,1996 Thirty nine papers and 32 posters from the January 1996 workshop assess the current status of parallel computing present recent developments and identify major trends More specifically they address technical issues connected with numerical algorithms communications programming tools parallel High-Performance VLSI Signal Processing Innovative Architectures and Algorithms, Algorithms and Architectures K. J. Ray Liu,Kung Yao,1998 Electrical Engineering Signal Processing High Performance VLSI Signal Processing Innovative Architectures and Algorithms Volume 1 Algorithms and Architectures The first volume in a two volume set High Performance VLSI Signal Processing Innovative Architectures and Algorithms brings together the most innovative papers in the field focused introductory material and extensive references The editors present timely coverage of algorithm and design methodologies with an emphasis on today s rapidly evolving high speed architectures for VLSI implementations These volumes will serve as vital resources for engineers who want a comprehensive knowledge of the extremely interdisciplinary field of high performance VLSI processing The editors provide a practical understanding of the merits of total system design through an insightful synergistic presentation

of methodology architecture and infrastructure Each volume features Major papers that span the wide range of research areas in the field Chapter introductions including historical perspectives Numerous applications oriented design examples Coverage of current and future technological trends Thorough treatment of high speed architectures

Computer Aided Systems Theory, 1999 **IEEE International Workshop on Rapid Systems Prototyping** IEEE Computer Society. Technical Committee on Simulation, 1999 Contains papers from a June 1999 workshop which brought together system designers model and tool developers integrated circuit designers and software engineers to explore problems and techniques in the area of rapid system prototyping Papers focus on models for system simulation emulation in a hierarchical sense software to hardware mapping software prototyping and validation prototyping environments of hardware simulators and experiences from specific system prototyping projects Contains sections on communication and distributed systems reconfigurable architectures reuse formal methods design methodologies interface technologies and FPGA based design Lacks a subject index Annotation copyrighted by Book News Inc Portland OR *Codesign* Jerzy Rozenblit, Klaus Buchenrieder, 1995

IEEE International Conference on Application-Specific Systems, Architectures and Processors Lothar Thiele (Computer scientist), 1997 These conference papers on computer hardware and design and testing include coverage of miscellaneous applications arithmetic algorithms and architectures DSP architectures systolic algorithms and architectures rapid prototyping design methodologies and compilers VLSI Signal Processing, VII Jan Rabaey, Paul M. Chau, John Eldon, 1994 Australian Computer Journal, 1994 **Proceedings**, 1994 **The Second International Conference on Expert Systems for Development** IEEE Computer Society. TC on Distributed Processing, 1994 A number of the selected papers from the Second International Conference March 1994 represent interesting applications however because the areas of artificial intelligence and expert systems have grown to maturity during the five years since the first conference an even larger number of papers discuss the theoretical principles underlying the

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