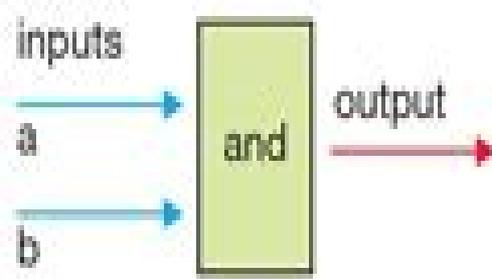


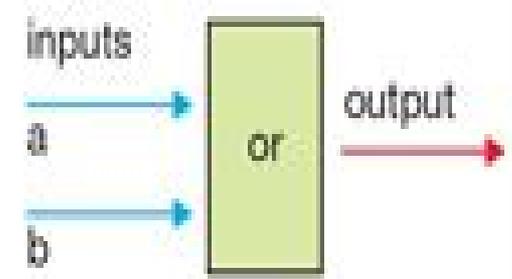
Logic circuits

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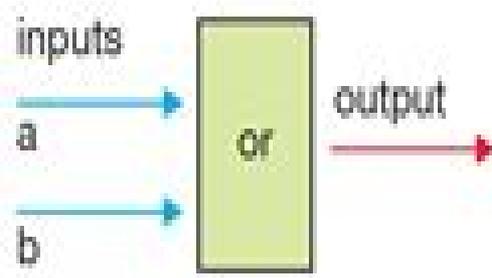
inputs		output
a	b	
0	0	0
0	1	0
1	0	0
1	1	1

EXCLUSIVE OR



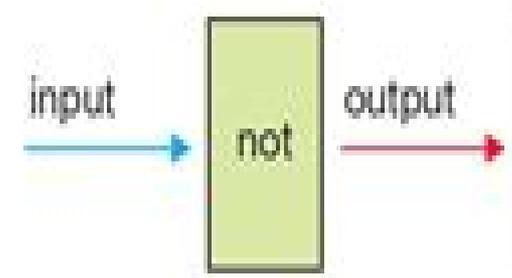
inputs		output
a	b	
0	0	0
0	1	1
1	0	1
1	1	0

OR



inputs		output
a	b	
0	0	0
0	1	1
1	0	1
1	1	1

NOT



input	output
0	1
1	0

Computer Logic Design

M. Morris Mano, Charles R. Kime



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Table of Contents Computer Logic Design

1. Understanding the eBook Computer Logic Design
 - The Rise of Digital Reading Computer Logic Design
 - Advantages of eBooks Over Traditional Books
2. Identifying Computer Logic Design
 - Exploring Different Genres
 - Considering Fiction vs. Non-Fiction
 - Determining Your Reading Goals
3. Choosing the Right eBook Platform
 - Popular eBook Platforms
 - Features to Look for in an Computer Logic Design
 - User-Friendly Interface
4. Exploring eBook Recommendations from Computer Logic Design
 - Personalized Recommendations
 - Computer Logic Design User Reviews and Ratings
 - Computer Logic Design and Bestseller Lists

5. Accessing Computer Logic Design Free and Paid eBooks
 - Computer Logic Design Public Domain eBooks
 - Computer Logic Design eBook Subscription Services
 - Computer Logic Design Budget-Friendly Options
6. Navigating Computer Logic Design eBook Formats
 - ePub, PDF, MOBI, and More
 - Computer Logic Design Compatibility with Devices
 - Computer Logic Design Enhanced eBook Features
7. Enhancing Your Reading Experience
 - Adjustable Fonts and Text Sizes of Computer Logic Design
 - Highlighting and Note-Taking Computer Logic Design
 - Interactive Elements Computer Logic Design
8. Staying Engaged with Computer Logic Design
 - Joining Online Reading Communities
 - Participating in Virtual Book Clubs
 - Following Authors and Publishers Computer Logic Design
9. Balancing eBooks and Physical Books Computer Logic Design
 - Benefits of a Digital Library
 - Creating a Diverse Reading Collection Computer Logic Design
10. Overcoming Reading Challenges
 - Dealing with Digital Eye Strain
 - Minimizing Distractions
 - Managing Screen Time
11. Cultivating a Reading Routine Computer Logic Design
 - Setting Reading Goals Computer Logic Design
 - Carving Out Dedicated Reading Time
12. Sourcing Reliable Information of Computer Logic Design
 - Fact-Checking eBook Content of Computer Logic Design
 - Distinguishing Credible Sources
13. Promoting Lifelong Learning

- Utilizing eBooks for Skill Development
 - Exploring Educational eBooks
14. Embracing eBook Trends
- Integration of Multimedia Elements
 - Interactive and Gamified eBooks

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