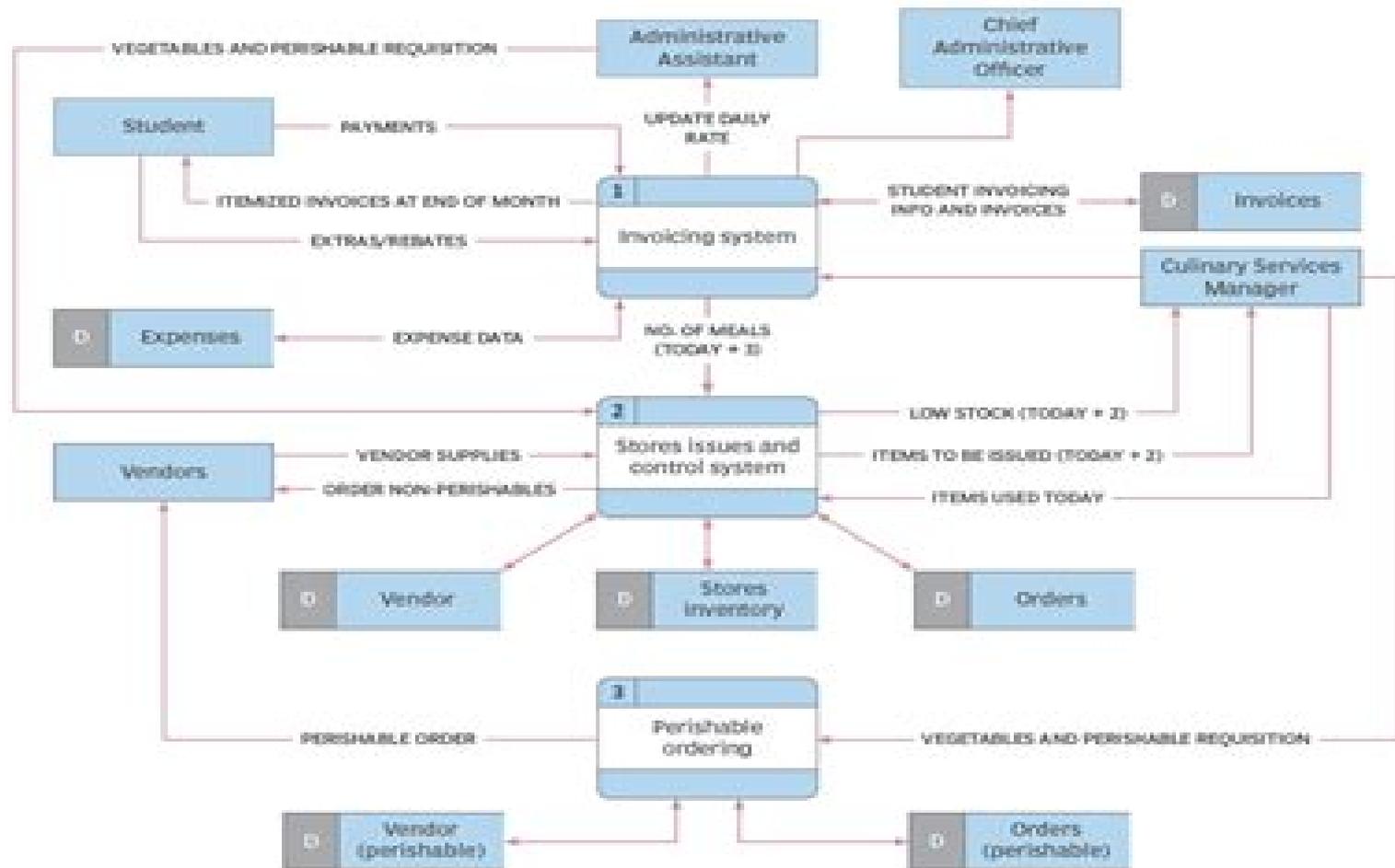


Example of a data flow diagram

Using the Gane & Sarson method, this data flow diagram shows the information system of a school's culinary program.



Data Flow Computing

John A. Sharp



Data Flow Computing:

Data Flow Computing John A. Sharp, 1985 This book is divided into four parts Part I discusses the ways in which computations can be modeled Part II builds on the formal model of computing introduced in the first section and discusses the implications for programming languages The implementation of the data flow model of computing and the various concepts necessary are discussed in Part III Part IV discusses how the data flow programming languages proposed could be implemented on the new machine architectures suggested in Part III

Data Flow Computing John A. Sharp, 1992 There is an increasing interest in data flow programming techniques This interest is motivated in part by the rapid advances in technology and the need for distributed processing techniques in part by a desire for faster throughput by applying parallel processing techniques and in part by search for a programming tool that is closer to the problem solving methods that people naturally adopts rather than current programming languages This book contains a selection of chapters by researchers on various aspects of the data flow approach in computing Topics covered include comparisons of various data flow machine designs data flow architectures intentional programming and operator nets and the relationship between data flow models and modern structured design techniques among others The book also includes a brief introduction to the data flow approach a bibliography and reviews of where research into data flow might be heading

Advanced Topics in Data-flow Computing Jean-Luc Gaudiot, Lubomir Bic, 1991

Data Flow Computing John A. Sharp, 1985

Soft Computing and Intelligent Systems Madan M. Gupta, 1999-10-28 The field of soft computing is emerging from the cutting edge research over the last ten years devoted to fuzzy engineering and genetic algorithms The subject is being called soft computing and computational intelligence With acceptance of the research fundamentals in these important areas the field is expanding into direct applications through engineering and systems science This book cover the fundamentals of this emerging filed as well as direct applications and case studies There is a need for practicing engineers computer scientists and system scientists to directly apply fuzzy engineering into a wide array of devices and systems

Encyclopedia of Parallel Computing David Padua, 2011-09-08 Containing over 300 entries in an A Z format the Encyclopedia of Parallel Computing provides easy intuitive access to relevant information for professionals and researchers seeking access to any aspect within the broad field of parallel computing Topics for this comprehensive reference were selected written and peer reviewed by an international pool of distinguished researchers in the field The Encyclopedia is broad in scope covering machine organization programming languages algorithms and applications Within each area concepts designs and specific implementations are presented The highly structured essays in this work comprise synonyms a definition and discussion of the topic bibliographies and links to related literature Extensive cross references to other entries within the Encyclopedia support efficient user friendly searchers for immediate access to useful information Key concepts presented in the Encyclopedia of Parallel Computing include laws and metrics specific numerical and non numerical algorithms asynchronous algorithms

libraries of subroutines benchmark suites applications sequential consistency and cache coherency machine classes such as clusters shared memory multiprocessors special purpose machines and dataflow machines specific machines such as Cray supercomputers IBM s cell processor and Intel s multicore machines race detection and auto parallelization parallel programming languages synchronization primitives collective operations message passing libraries checkpointing and operating systems Topics covered Speedup Efficiency Isoefficiency Redundancy Amdahls law Computer Architecture Concepts Parallel Machine Designs Benmarks Parallel Programming concepts design Algorithms Parallel applications This authoritative reference will be published in two formats print and online The online edition features hyperlinks to cross references and to additional significant research Related Subjects supercomputing high performance computing distributed computing

PARALLEL AND DISTRIBUTED COMPUTING BASU, S. K. ,2016-01-02 This concise text is designed to present the recent advances in parallel and distributed architectures and algorithms within an integrated framework Beginning with an introduction to the basic concepts the book goes on discussing the basic methods of parallelism exploitation in computation through vector processing super scalar and VLIW processing array processing associative processing systolic algorithms and dataflow computation After introducing interconnection networks it discusses parallel algorithms for sorting Fourier transform matrix algebra and graph theory The second part focuses on basics and selected theoretical issues of distributed processing Architectures and algorithms have been dealt in an integrated way throughout the book The last chapter focuses on the different paradigms and issues of high performance computing making the reading more interesting This book is meant for the senior level undergraduate and postgraduate students of computer science and engineering and information technology The book is also useful for the postgraduate students of computer science and computer application Key features Each chapter is explained with examples or example systems as the case may be to make the principles methods involved easily understandable Number of exercises are given at the end of each chapter for helping the reader to have better understanding of the topics covered A large number of journal articles are highlighted to help the students interested in studying further in this field

[Guide to DataFlow Supercomputing](#) Veljko Milutinović, Jakob Salom, Nemanja Trifunovic, Roberto Giorgi, 2015-04-28 This unique text reference describes an exciting and novel approach to supercomputing in the DataFlow paradigm The major advantages and applications of this approach are clearly described and a detailed explanation of the programming model is provided using simple yet effective examples The work is developed from a series of lecture courses taught by the authors in more than 40 universities across more than 20 countries and from research carried out by Maxeler Technologies Inc Topics and features presents a thorough introduction to DataFlow supercomputing for big data problems reviews the latest research on the DataFlow architecture and its applications introduces a new method for the rapid handling of real world challenges involving large datasets provides a case study on the use of the new approach to accelerate the Cooley Tukey algorithm on a DataFlow machine includes a step by step guide to

the web based integrated development environment WebIDE *Encyclopedia of Computer Science and Technology* Allen Kent, James G. Williams, 1992-03-20 This comprehensive reference work provides immediate fingertip access to state of the art technology in nearly 700 self contained articles written by over 900 international authorities Each article in the Encyclopedia features current developments and trends in computers software vendors and applications extensive bibliographies of leading figures in the field such as Samuel Alexander John von Neumann and Norbert Wiener and in depth analysis of future directions

Cyber Resilience Sergei Petrenko, 2022-09-01 Modern cyber systems acquire more emergent system properties as far as their complexity increases cyber resilience controllability self organization proactive cyber security and adaptability Each of the listed properties is the subject of the cybernetics research and each subsequent feature makes sense only if there is a previous one Cyber resilience is the most important feature of any cyber system especially during the transition to the sixth technological stage and related Industry 4.0 technologies Artificial Intelligence AI Cloud and foggy computing 5G IoT IIoT Big Data and ETL Q computing Blockchain VR AR etc We should even consider the cyber resilience as a primary one because the mentioned systems cannot exist without it Indeed without the sustainable formation made of the interconnected components of the critical information infrastructure it does not make sense to discuss the existence of 4.0 Industry cyber systems In case when the cyber security of these systems is mainly focused on the assessment of the incidents probability and prevention of possible security threats the cyber resilience is mainly aimed at preserving the targeted behavior and cyber systems performance under the conditions of known about 45 % as well as unknown the remaining 55 % cyber attacks This monograph shows that modern Industry 4.0 Cyber systems do not have the required cyber resilience for targeted performance under heterogeneous mass intruder cyber attacks The main reasons include a high cyber system structural and functional complexity a potential danger of existing vulnerabilities and sleep hardware and software tabs as well as an inadequate efficiency of modern models methods and tools to ensure cyber security reliability response and recovery

Data Flow Machine for Data Driven Computing, 1995 A data flow computer which of computing is disclosed which utilizes a data driven processor node architecture The apparatus in a preferred embodiment includes a plurality of First In First Out FIFO registers a plurality of related data flow memories and a processor The processor makes the necessary calculations and includes a control unit to generate signals to enable the appropriate FIFO register receiving the result In a particular embodiment there are three FIFO registers per node an input FIFO register to receive input information from an outside source and provide it to the data flow memories an output FIFO register to provide output information from the processor to an outside recipient and an internal FIFO register to provide information from the processor back to the data flow memories The data flow memories are comprised of four commonly addressed memories A parameter memory holds the A and B parameters used in the calculations an opcode memory holds the instruction a target memory holds the output address and a tag memory contains status bits for each parameter One status bit indicates whether

the corresponding parameter is in the parameter memory and one status bit to indicate whether the stored information in the corresponding data parameter is to be reused. The tag memory outputs a fire signal signal R VALID when all of the necessary information has been stored in the data flow memories and thus when the instruction is ready to be fired to the processor.

Computers for Artificial Intelligence Processing Benjamin W. Wah, C. V. Ramamoorthy, 1990-10-12. The present book supports the increasing complexity and the growing need for computational power of artificial intelligence AI by providing comprehensive treatments of new hardware and software engineering met in AI language design and applications. The book is a collection of 16 substantial papers, chapters, the contributors being 51 well known researchers in the AI fields. The papers are grouped into the following five sections: Section 1 represents a well documented survey on symbolic processing computers; Section 2 Language based AI Architectures supports the design and implementation of AI language oriented computers. Three 2 4 chapters are devoted to computer architecture concerning sequential Lisp processing architectural features of Lisp computers. Symbolics Lisp computer architecture memory management and performance evaluation of a Lisp machine system. Other three 5 7 chapters discuss multiprocessing and parallel processing of Lisp and general functional programs. The last two chapters of section 2 are presenting architectures supporting object oriented programming Smalltalk and production systems. Section 3 Multiprocessor AI Architecture contains two 10 11 chapters dealing with Connection Machine architecture and its applications and with the design of data knowledge base machines for AI processing. Section 4 Connectionist Architectures and Applications include two 12 13 chapters illustrating the connectionist model architecture design and learning. Section 5 Software Architectures for AI Applications is made up of three 14 16 chapters analysing the relationship between AI and software engineering, the development tools for AI programs and the problem of AI hardware and software reliability. This book addresses a wide range of AI readers from beginners to professionals. It carries forth doubtless qualities compact and well dimensioned chapters comprehensively written by AI remarkable professionals covering up to date AI topics and trends.

Advances in Computer Architecture Glenford J. Myers, 1982. A completely updated edition of this overview of modern computer architecture. Examines alternatives to classical low level von Neumann computer architecture discussing the problems of classical architecture and new solutions to these problems. Illustrates new concepts through in depth case studies of the Intel APX 432 IBM's SWARD and other machines. State of the art concepts covered include tagged storage capability based addressing process management protection domains and error detection.

New Generation Computer Systems Yungui Ci, 1989. Proceedings of the international conference held in Beijing April 17 21 1989. Topics covered include knowledge representation knowledge base systems intelligent environment knowledge acquisition on parallel architecture computation models Prolog machines knowledge base machines functional.

Laboratory for Computer Science Progress Report Massachusetts Institute of Technology. Laboratory for Computer Science, 1987.

Handbook of Software Engineering Charles Ralph Vick, Chittoor V.

Ramamoorthy,1984 **Advanced Topics in Dataflow Computing and Multithreading** Guang R. Gao,Lubomir
Bic,Jean-Luc Gaudiot,1995-07-14 The book includes papers on massively parallel distributed memory and multithreaded
architecture design synchronization and pipelined design and superpipelined data driven VLSI processors Other sections
discuss stream data types the development of well structured software and parallelization of dataflow programs First
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