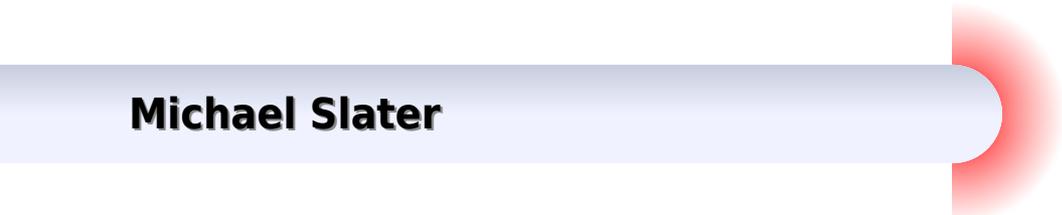


Designing With Microprocessors

Michael Slater



Designing With Microprocessors:

The Design of a Microprocessor Wilhelm G. Spruth, 2012-12-06 This text has been produced for the benefit of students in computer and information science and for experts involved in the design of microprocessors It deals with the design of complex VLSI chips specifically of microprocessor chip sets The aim is on the one hand to provide an overview of the state of the art and on the other hand to describe specific design know how The depth of detail presented goes considerably beyond the level of information usually found in computer science text books The rapidly developing discipline of designing complex VLSI chips especially microprocessors requires a significant extension of the state of the art We are observing the genesis of a new engineering discipline the design and realization of very complex logical structures and we are obviously only at the beginning This discipline is still young and immature alternate concepts are still evolving and the best way to do it is still being explored Therefore it is not yet possible to describe the different methods in use and to evaluate them However the economic impact is significant today and the heavy investment that companies in the USA the Far East and in Europe are making in generating VLSI design competence is a testimony to the importance this field is expected to have in the future Staying competitive requires mastering and extending this competence

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High-Performance Energy-Efficient Microprocessor Design Vojin G. Oklobdzija,Ram K. Krishnamurthy,2007-04-27 Microprocessors of today contain close to a billion transistors while achieving the performance of super computers just a decade ago Designing such processors takes hundreds of people organized into large teams High Performance Energy Efficient Microprocessor Design is written by the world s most prominent microprocessor design leaders from the industry and academia It provides a complete coverage of all the aspects of a complex microprocessor design process from technology power management clocking high performance architecture design methodologies memory and I O design computer aided design testing and design for testability The chapters are written to provide the latest state of the art knowledge of particular aspects of microprocessor design while including sufficient tutorial content in order to bring non experts up to speed High Performance Energy Efficient Microprocessor Design is intended to be a useful companion book for every design engineer working in the related areas and a source of technical information as well as a comprehensive reference in the field It should also serve as the source book for technical and business managers involved in microprocessor based design and manufacture The chapters are organized in a way which makes it possible to use this book as a textbook for graduate courses in advanced digital and system design The book is intended to highlight practical problems encountered in designing state of the art processors while yet covering fundamental principles that are independent of technology

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